

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Daisuke KATO et al.

Serial No: Not assigned (parent 10/053,524)

Filed: September 17, 2003

For: Semiconductor Storage Device Formed
to Optimized Test Technique and
Redundancy Technology

Art Unit: Not assigned

Examiner: Not assigned

PRELIMINARY AMENDMENT

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Prior to the first Office Action in the present application, please enter and consider the following amendments and remarks:

Amendments to the specification begin on page 2 of this paper.

Amendments to the Abstract: Replace the abstract with a new abstract as set forth in a separate sheet attached hereto.

Remarks/Arguments begin on page 10 of this paper.